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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. 7,033,437 B2)
MEARS ET AL.)
Issued: APRIL 25, 2006)
For: METHOD FOR MAKING SEMICONDUCTOR)
DEVICE INCLUDING BAND-)
ENGINEERED SUPERLATTICE)

)

*Certificate
of Correction
AUG 03 2006*

REQUEST FOR CERTIFICATE OF CORRECTION OF
APPLICANT'S MISTAKE UNDER RULE 1.323

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of Rule 1.323 of the Rules of Practice, the Patent Office is respectfully requested to correct matters in the above-identified patent in accordance with the Certificate of Correction Form PTO1050 attached hereto.

The Patent Office is requested to issue a Certificate of Correction on the above-referenced Patent Document, and to place such a Certificate of Correction in the file, so that such will appear on any copies of the patent which are ordered in the future.

The corrections being made are of consequence to provide the proper recitation on the cover page, in the drawings and specification. Several of the mistakes occurred in good faith and are not the fault of the Patent and Trademark Office. However, two printing errors of the Patent Office are also being corrected. No new matter is added by these corrections.

08/01/2006 YPOLITE1 00000108 7033437

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100.00 OP

AUG 3 2006

In re Patent of
MEARS ET AL.
Patent No. **7,033,437 B2**
Issued: **APRIL 25 2006**

Authorization is given to charge the requisite fee as set forth in 37 CFR § 1.20(a) in the amount of \$100.00 to the credit card listed on the attached credit card payment form PTO 2038. The Commissioner is authorized to charge or credit any discrepancies in fee amounts to Deposit Account 010484.

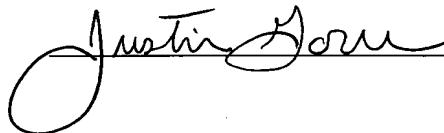
Respectfully submitted,



CHRISTOPHER F. REGAN
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& Gilchrist, P.A.
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, on this 26th day of July, 2006.



APR 2 2006

UNITED STATES PATENT AND TRADEMARK OFFICE

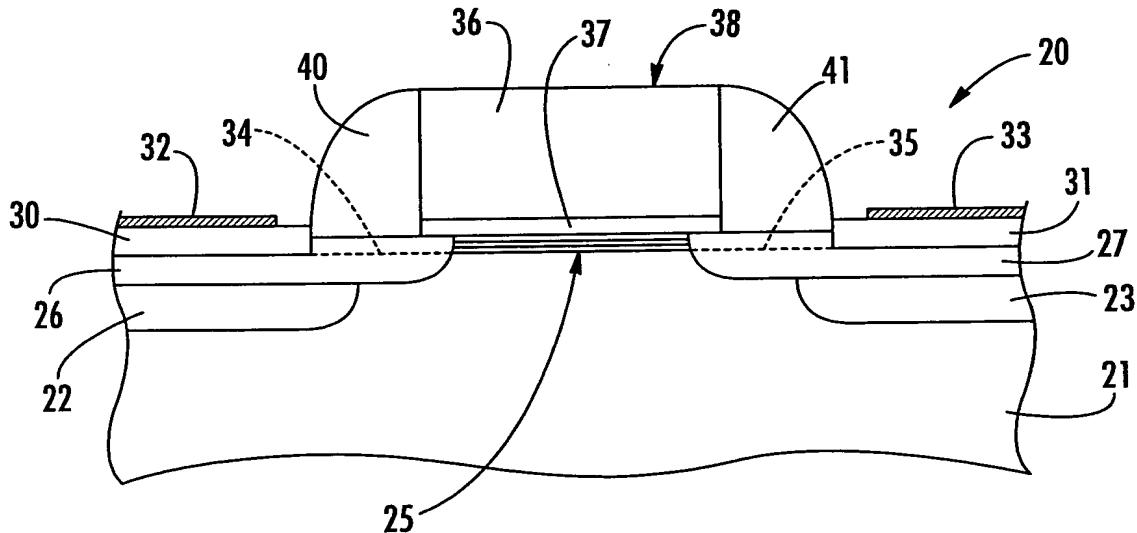
CERTIFICATE OF CORRECTION

PATENT NO.	:	7,033,437 B2
DATED	:	April 25, 2006
INVENTOR(S)	:	Robert J. Mears, Jean Augustin Chan Sow Fook Yiptong, Marek Hytha, Scott A. Kreps, Ilija Dukovski

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below

Cover Page,
Drawing

Delete: FIG. 1
Insert: New FIG. 1



MAILING ADDRESS OF SENDER:

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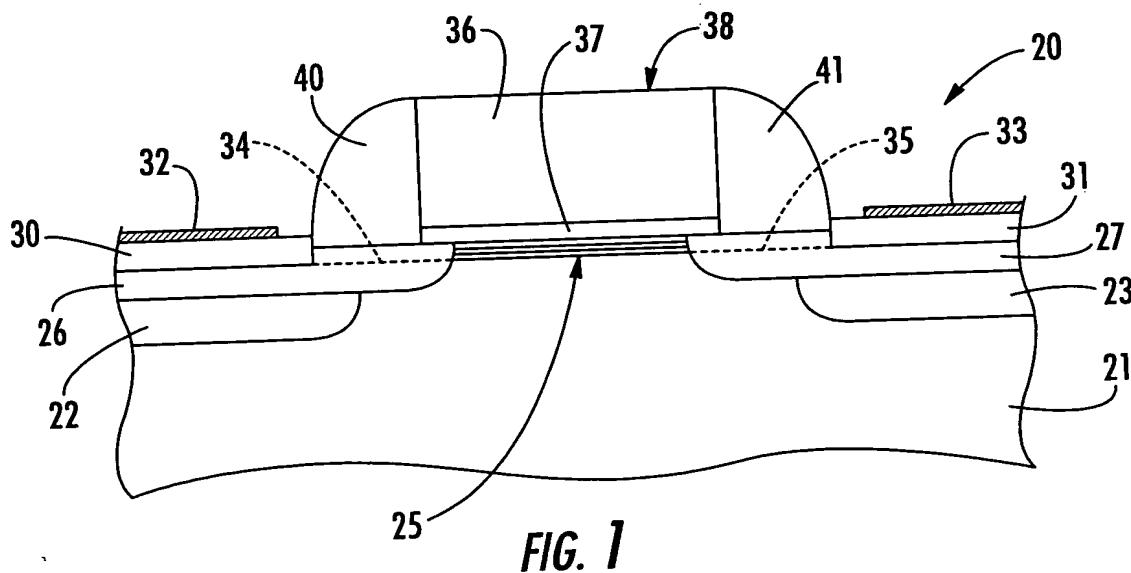
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In the Drawings

Delete: FIG. 1
 Insert: New FIG. 1



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Column 2, Line 2 Delete: "in a silicon"
 Insert: --in silicon--

Column 2, Line 3 Delete: "electromuminescence"
 Insert: --electroluminescence--

Column 2, Line 62 Delete: "superlattice and has"
 Insert: --superlattice has--

Column 4, Line 35 Delete:

$$\mathbf{M}_{h,i,j}^{-1}(E_F, T) = \frac{- \sum_{E > E_F \text{ B.Z.}} \int (\nabla_{\mathbf{k}} E(\mathbf{k}, n))_i (\nabla_{\mathbf{k}} E(\mathbf{k}, n))_j \frac{\partial f(E(\mathbf{k}, n), E_F, T)}{\partial E} d^3\mathbf{k}}{\sum_{E > E_F \text{ B.Z.}} \int (1 - f(E(\mathbf{k}, n), E_F, T)) d^3\mathbf{k}}$$

Insert:

$$\mathbf{M}_{h,i,j}^{-1}(E_F, T) = \frac{- \sum_{E < E_F \text{ B.Z.}} \int (\nabla_{\mathbf{k}} E(\mathbf{k}, n))_i (\nabla_{\mathbf{k}} E(\mathbf{k}, n))_j \frac{\partial f(E(\mathbf{k}, n), E_F, T)}{\partial E} d^3\mathbf{k}}{\sum_{E < E_F \text{ B.Z.}} \int (1 - f(E(\mathbf{k}, n), E_F, T)) d^3\mathbf{k}}$$

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Column 5, Line 18	Delete: "gate 35" Insert: --gate 38--
Column 5, Line 67	Delete: "gate 35" Insert: --gate 38--
Column 8, Line 4	Delete: "from the both" Insert: --from both--
Column 9, Line 67	Delete: "also formed" Insert: --also be formed--

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